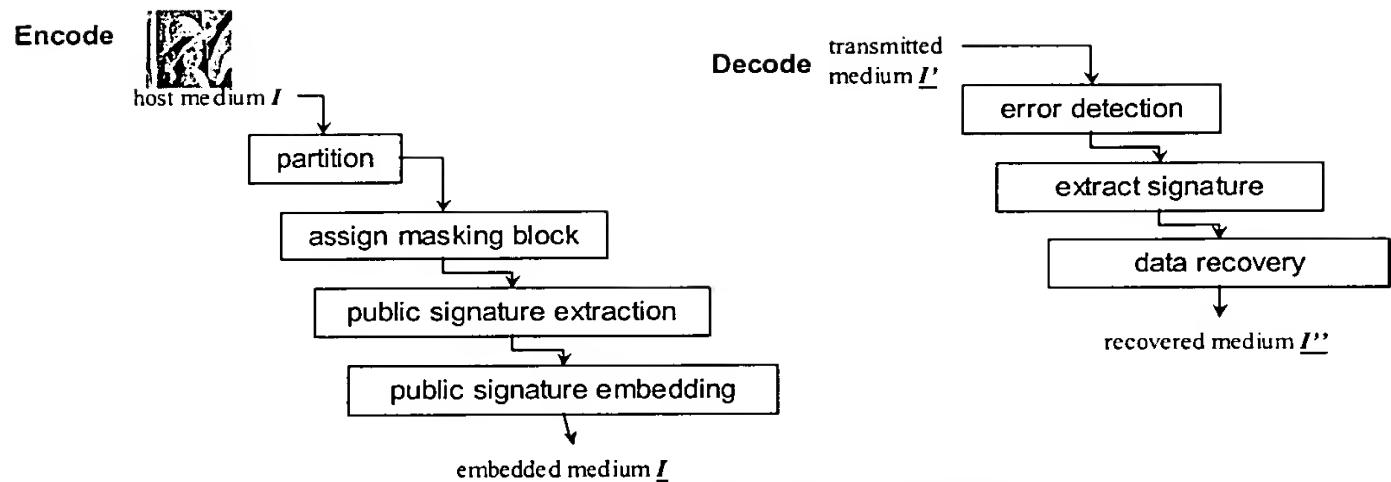


**Fig. 1**



**Fig. 2**



**Fig 2. General architecture**

Fig. 3

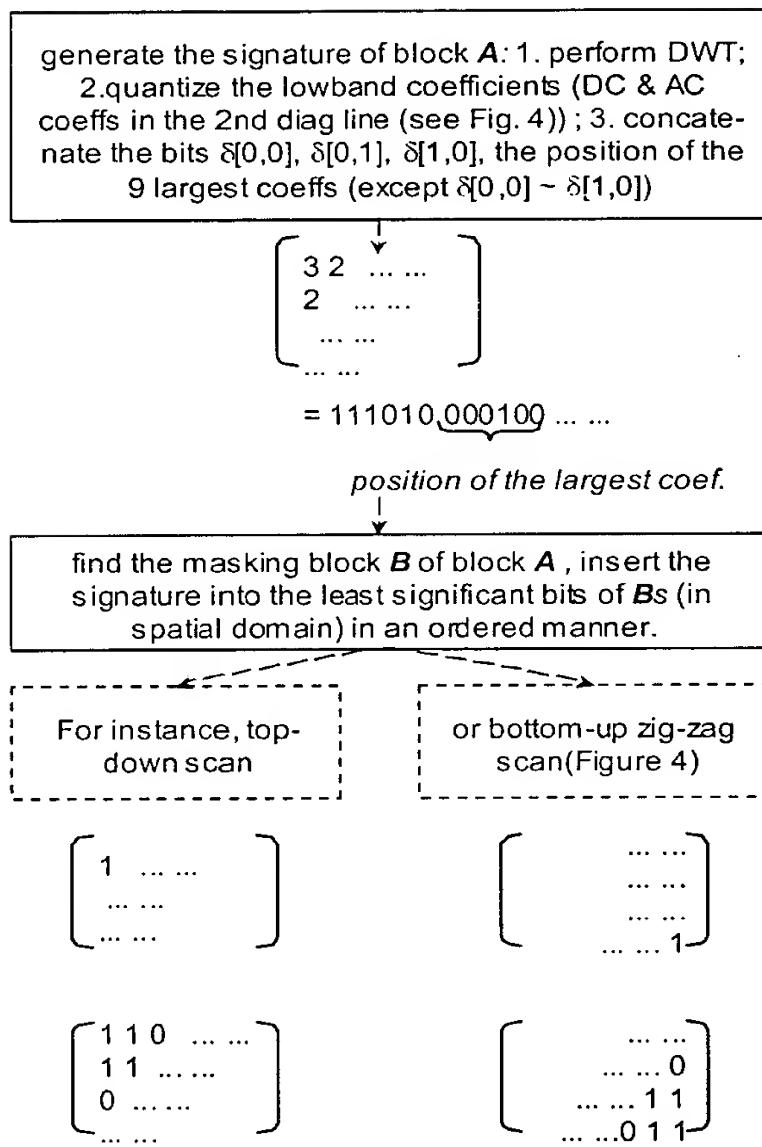
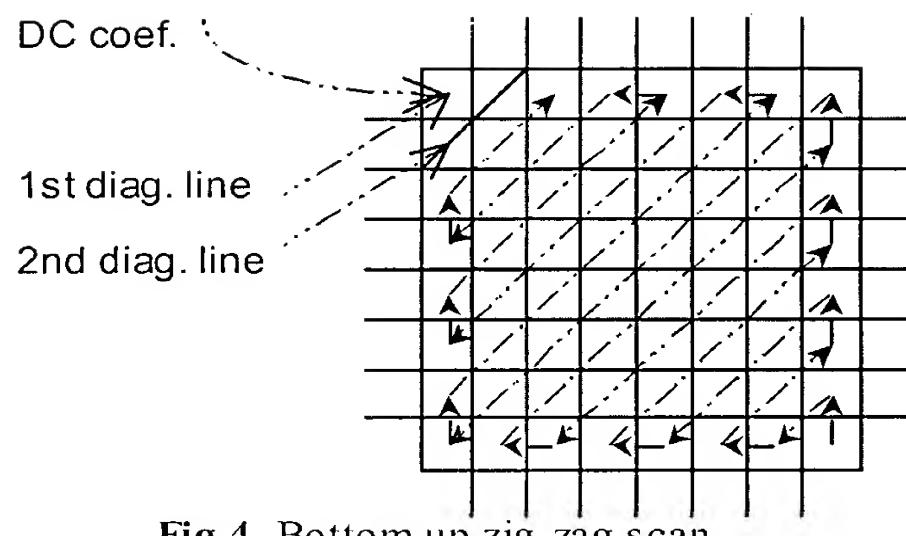


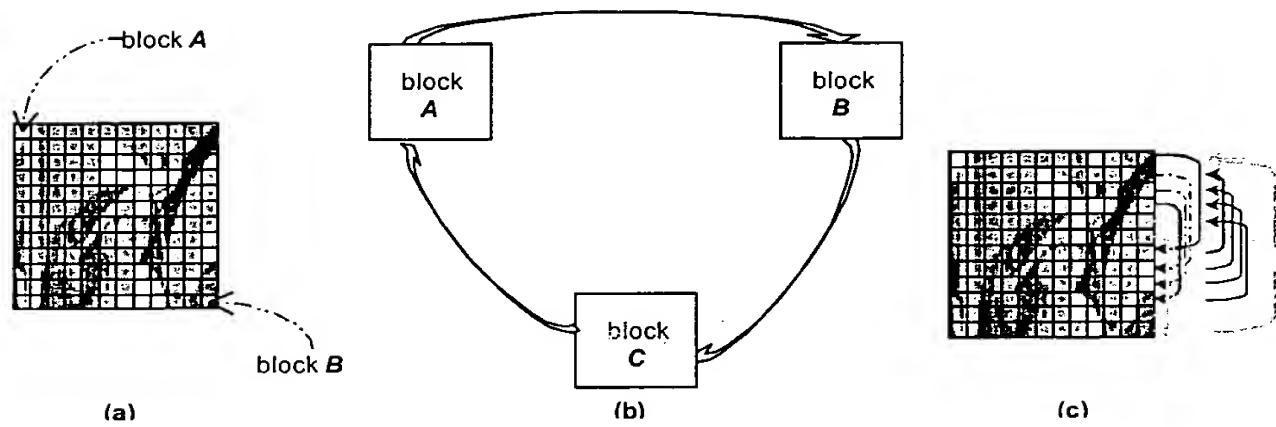
Fig 3. Embedding strategy

**Fig. 4**

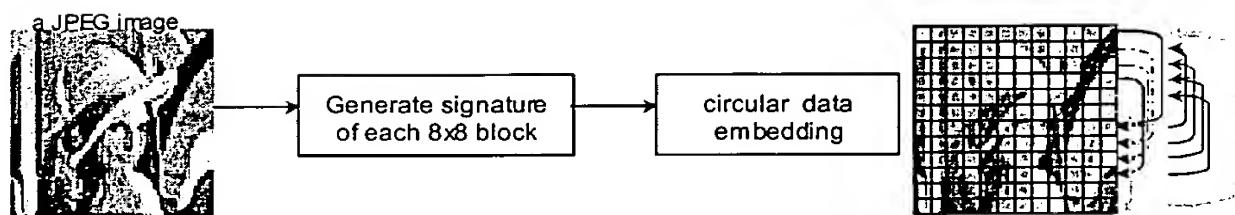


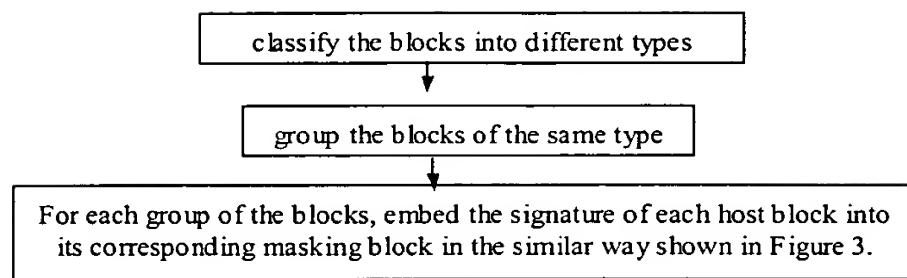
**Fig 4.** Bottom up zig-zag scan

Figs 5a 5b 5c

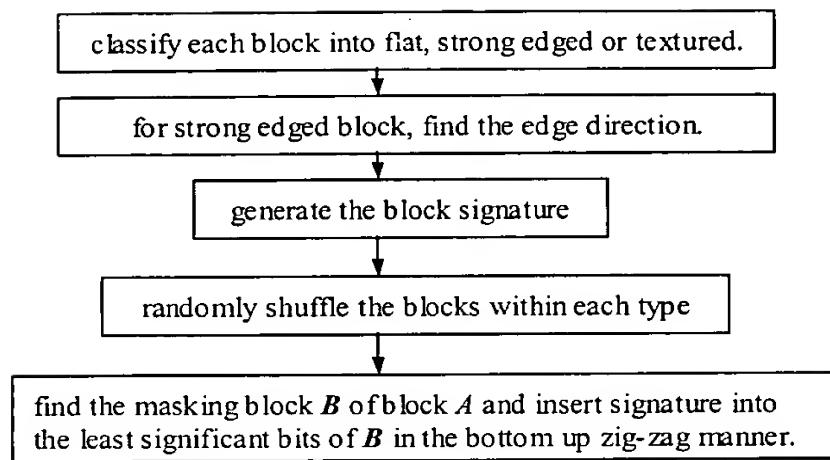


**Fig. 6**





**Fig 7**



**Fig 8**